## Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

## **Listing of Claims:**

What is claimed is:

1. (Currently amended) A method for high speed USB data routing, the method comprising:

routing a data stream to and from USB I/O ports serially; [and]

maintaining a frequency of the data stream during the routing[.]; and

routing from a root port downstream to at least one I/O port and from one I/O port

upstream to the root port.

- 2. (Original) The method of claim 1 further comprising providing a root port router for the root port and a data port router for each I/O port, wherein each data port router delays the data stream by one bit during the routing.
- 3. (Original) The method of claim 1 wherein routing a data stream further comprises routing on a two-bit wire, the two-bit wire carrying a data bit and a corresponding enable bit for each bit of the data stream.
  - 4. (Canceled)

- 5. (Original) The method of claim 1 further comprising performing the routing in a USB hub.
- 6. (Original) The method of claim 2 further comprising performing the routing with up to seven I/O ports.
- 7. (Original) The method of claim 1 wherein maintaining the frequency of the data stream during the routing further comprises maintaining the frequency at 480MHz.
- 8. (Currently amended) A system for high speed USB data routing, the system comprising:

a plurality of USB I/O ports; [and]

a plurality of routers coupled to the plurality of USB I/O ports for routing a data stream to and from the USB I/O ports serially and maintaining a frequency of the data stream during the routing[.]; and

a plurality of routers route from a root port downstream to at least one I/O port and from one I/O port upstream to the root port.

9. (Original) The system of claim 8 wherein the plurality of routers further comprise a root port router for the root port and a data port router for each I/O port, wherein each data port router delays the data stream by one bit during the routing.

10. (Original) The system of claim 8 wherein the plurality of routers route on a two-bit wire, the two-bit wire carrying a data bit and a corresponding enable bit for each bit of the data stream.

## 11. (Cancelled)

- 12. (Original) The system of claim 8 wherein the plurality of I/O ports and the plurality of routers further comprise a router portion of a USB hub.
- 13. (Original) The system of claim 9 wherein the plurality of I/O ports further comprise up to seven I/O ports.
- 14. (Original) The system of claim 8 wherein the frequency of the data stream further comprises 480MHz.
- 15. (Currently amended) A method for high speed USB data routing, the method comprising:

providing a root port router for a root port of a USB hub;

providing a data port router for each I/O port of the USB hub; [and]

routing data of a data stream serially between the root port router and each data port router without altering a frequency of the data stream[.]; and

routing data downstream from the root port through the root port router and through each of the data port routers to at least one I/O port.

## 16. (Cancelled)

- 17. (Currently amended) The method of claim [16] 15 wherein routing data further comprises routing data upstream from one I/O port through each data port router and through the root port router to the root port.
- 18. (Original) The method of claim 15 wherein each data port router and the root port router delays the data stream by one bit during the routing.
- 19. (Currently amended) The method of claim [5] 15 wherein the frequency of the data stream further comprises 480MHz.
- 20. (Original) The method of claim 15 further comprising providing a data control block for the data port router and each I/O port to control enabling of each I/O port during the routing.